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Attorney Docket SEL 255



#12/13
Jeh
5/13/03

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Local Application of

Yamazaki et al.

Serial No.: 09/841,537

Filed: April 24, 2001

For: Semiconductor Device And Method
Of Fabricating Thereof

Art Unit: 2822

Examiner: M. Prenty

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to:
The Assistant Commissioner for Patents,
P.O. Box 1450, Alexandria, VA 22313-1450 on

May 1, 2003

(Date of Deposit)

Shannon Wallace

Name of applicant, assignee, or Registered Rep.

Shannon Wallace 5/1/03

Signature

Date

AMENDMENT B

Sir:

In response to the Office Action dated January 3, 2003, a one month extension of time being separately submitted, please amend the above-identified application as follows:

TECHNOLOGY CENTER 2800

MAY - 9 2003

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IN THE CLAIMS:

Please amend the claims as follows:

Marked-up copy of the claims as amended:

1.(Amended) A semiconductor device comprising:

a thin film transistor comprising a semiconductor layer over a substrate and a gate electrode with an insulating film interposed therebetween;

a plurality of projected portions over said substrate;

Object
P/C1